深圳市勋瑞光电科技有限公司
Xunrui photoelectric technology （shenzhen）CO,.LTD.

Product Specification

Customer: _____

Model Name: H043BWV40I3082

Date: 2024-01-08

Version: V0

■ **Preliminary Specification**

☐ **Final Specification**

For Customer's Acceptance

Approved by	Comment

Approved by	Reviewed by	Prepared by

Contents

1. Record of Revision

Version	Revise Date	Content	Editor
V0	2024/01/08	First Release.	

2 General Specifications

Feature		Spec
Characteristics	LCD Size	4.3 inch
	Display Format	480 (RGB) ×800
	Interface	SPI+RGB
	Color Depth	16.7M
	Technology type	a-Si
	Display Spec.	0.039(RGB)*0.117
	Display Mode	Normally Black
	Driver IC	ST7701S
	Surface Treatment	HC
	Viewing Direction	ALL
	Gray Viewing Direction	FREE
Mechanical	LCM (W x H x D) (mm)	60.86*102.57*2.70
	Active Area(mm)	56.16x 93.60
	With /Without TSP	Without TSP
	Weight (g)	TBD
	LED Numbers	10 LEDs

Note 1: Viewing direction is following the data which measured by optics equipment.
Note 2: Requirements on Environmental Protection: RoHS
Note 3: LCM weight tolerance: +/- 5%

3 Input/Output Terminals

Pin NO.	Symbol	Description
1	LEDK	back light power supply negative
2	LEDA	back light power supply positive
3	GND	Ground
4	VCC	Power supply
5-12	R0-R7	Red Data
13-20	G0-G7	Green Data
21-28	B0-B7	Blue Data
29	GND	Ground
30	CLK	Colock signal
31	DISP	Display on/off
32	HSYNC	Horizontal sync input in RGB mode(short to GND if not used)
33	VSYNC	Vertical sync input in RGB mode(short to GND if not used)
34	DE	Data enable
35	NC	No Connection
36	GND	Ground
37	RESET	Reset Signal pin
38	SDA	Serial data input/output bidirectional pin for SPI interface
39	SCL	Serial clock input for SPI interface
40	CS	A Chip Select signal

4 Absolute Maximum Ratings

Item	Symbol	MIN	MAX	Unit	Remark
Power supply voltage (VDD)	V_{CC}	-0.3	+4.6	V	
Logic Input Voltage Range	VIN	-0.3	VCC + 0.3	V	
Logic Output Voltage Range	VO	-0.3	VCC + 0.3	V	
Operating Temperature	T_{OPR}	-20	70	℃	
Storage Temperature	T_{STG}	-30	80	℃	

5 Electrical Characteristics

5.1 Operating conditions:

Parameter	Symbol	MIN	TYP	MAX	Unit	Remark
Power Voltage	V_{CC}	2.5	2.8	3.6	V	
Digital Operation Current	Icc	-	25	-	mA	
Sleep-in mode	Icc	5	45	60	uA	

5.2 Driving Backlight

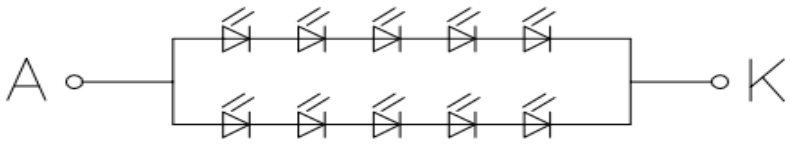
Item	Symbol	MIN	TYP	MAX	Unit	Remark
LED current	I_F	-	40	-	mA	
Power Consumption	P_{WL}	-	600	-	mW	
LED Voltage	V_F	14.5	15.0	16.5	V	Note 1
LED Life Time		25000	-	-	Hr	Note 2,3
Colour temperature			-		K	

Note 1 : There are 2 Groups LED

Note 2 : Ta = 25℃

Note 3 : Brightess to be decreased to 50% of the initial value

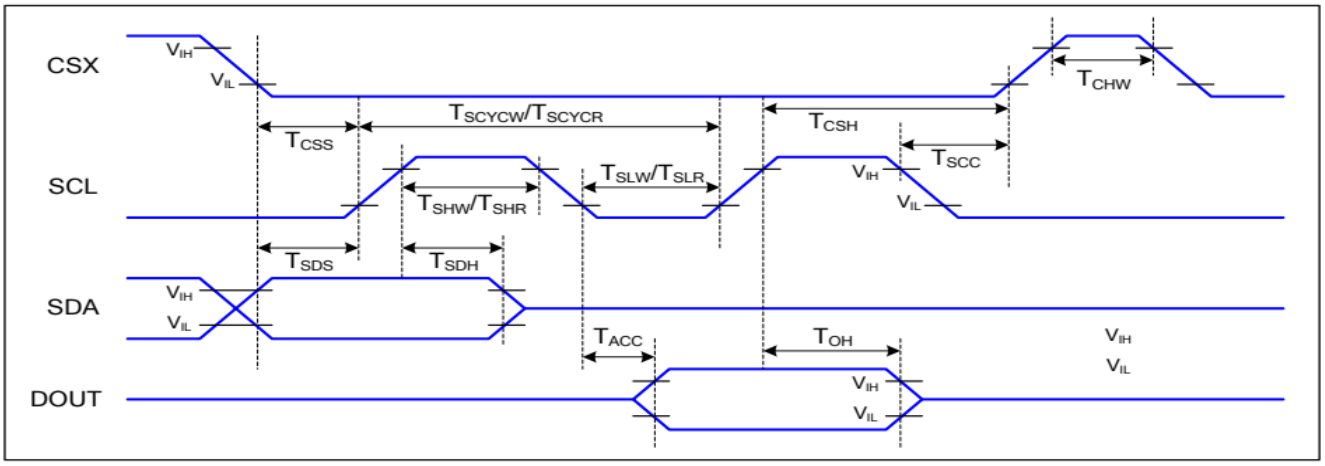
LED CIRCUIT DIAGRAM



IF=40MA, VF=14.5~16.5V

6 Interface Timing

6.1Serial Interface Characteristics (3-line serial):



VDDI=1.8,VDD=2.8, AGND=DGND=0V, Ta=25℃

Signal	Symbol	Parameter	Min	Max	Unit	Description
CSX	T_{CSS}	Chip select setup time (write)	15		ns	
	T_{CSH}	Chip select hold time (write)	15		ns	
	T_{CSS}	Chip select setup time (read)	60		ns	
	T_{SCC}	Chip select hold time (read)	60		ns	
	T_{CHW}	Chip select “H” pulse width	40		ns	
SCL	T_{SCYCW}	Serial clock cycle (Write)	66		ns	
	T_{SHW}	SCL “H” pulse width (Write)	15		ns	
	T_{SLW}	SCL “L” pulse width (Write)	15		ns	
	T_{SCYCR}	Serial clock cycle (Read)	150		ns	
	T_{SHR}	SCL “H” pulse width (Read)	60		ns	
	T_{SLR}	SCL “L” pulse width (Read)	60		ns	
SDA (DIN)	T_{SDS}	Data setup time	10		ns	
	T_{SDH}	Data hold time	10		ns	

Table 4 3-line serial Interface Characteristics

Note : The rising time and falling time (Tr, Tf) of input signal are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for Input signals.

6.2 RGB Interface Characteristics :

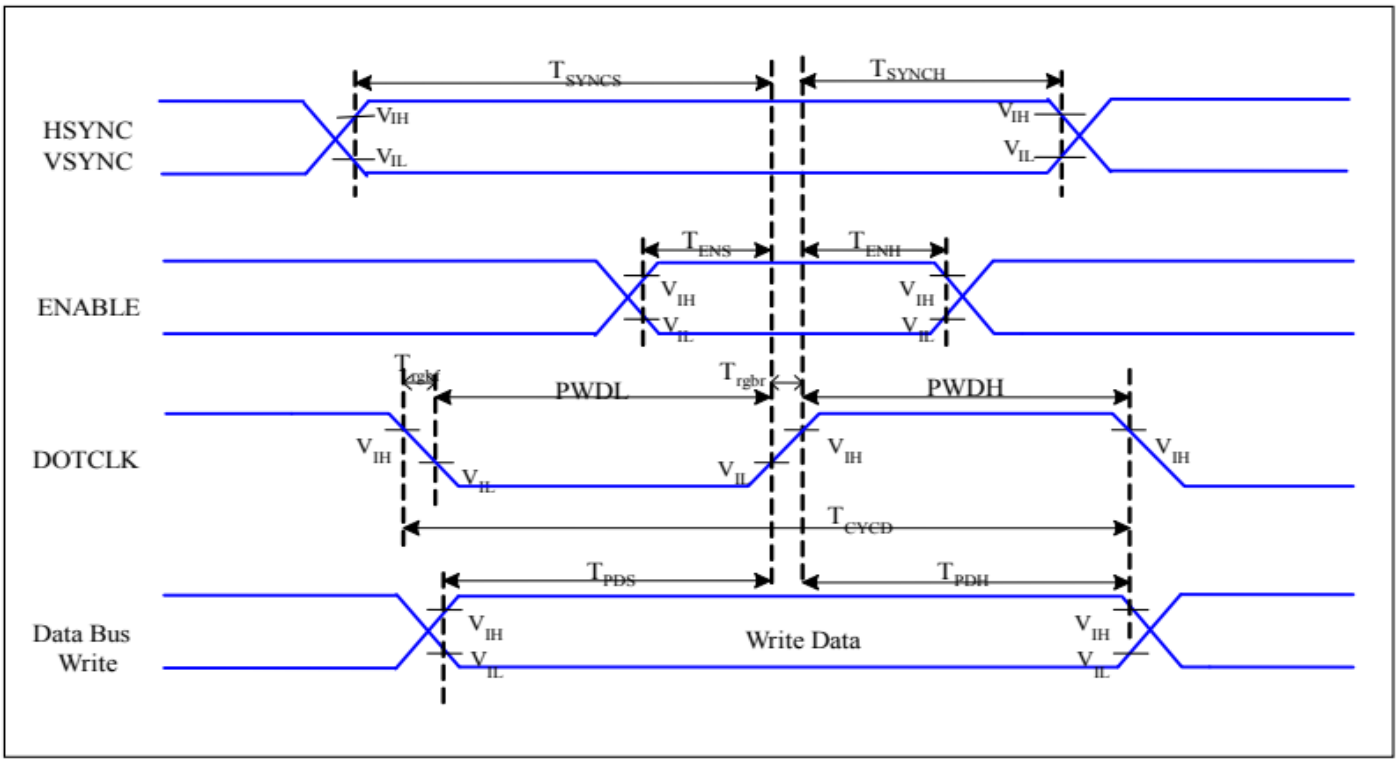


Figure 3 RGB Interface Timing Characteristics

VDDI=1.8,VDD=2.8, AGND=DGND=0V, Ta=25 ℃

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	T_{SYNCS}	VSYNC, HSYNC Setup Time	5	-	ns	
ENABLE	T_{ENS}	Enable Setup Time	5	-	ns	
	T_{ENH}	Enable Hold Time	5	-	ns	
DOTCLK	PWDH	DOTCLK High-level Pulse Width	15	-	ns	
	PWDL	DOTCLK Low-level Pulse Width	15	-	ns	
	T_{CYCD}	DOTCLK Cycle Time	33	-	ns	
	Trghr, Trghf	DOTCLK Rise/Fall time	-	15	ns	
DB	T_{PDS}	PD Data Setup Time	5	-	ns	
	T_{PDH}	PD Data Hold Time	5	-	ns	

Table 6 18/16 Bits RGB Interface Timing Characteristics

6.3 RGB Interface Definition

The display operation via the RGB interface is synchronized with the VSYNC, HSYNC, and DOTCLK signals. The data can be written only within the specified area with low power consumption by using window address function. The back porch and front porch are used to set the RGB interface timing.

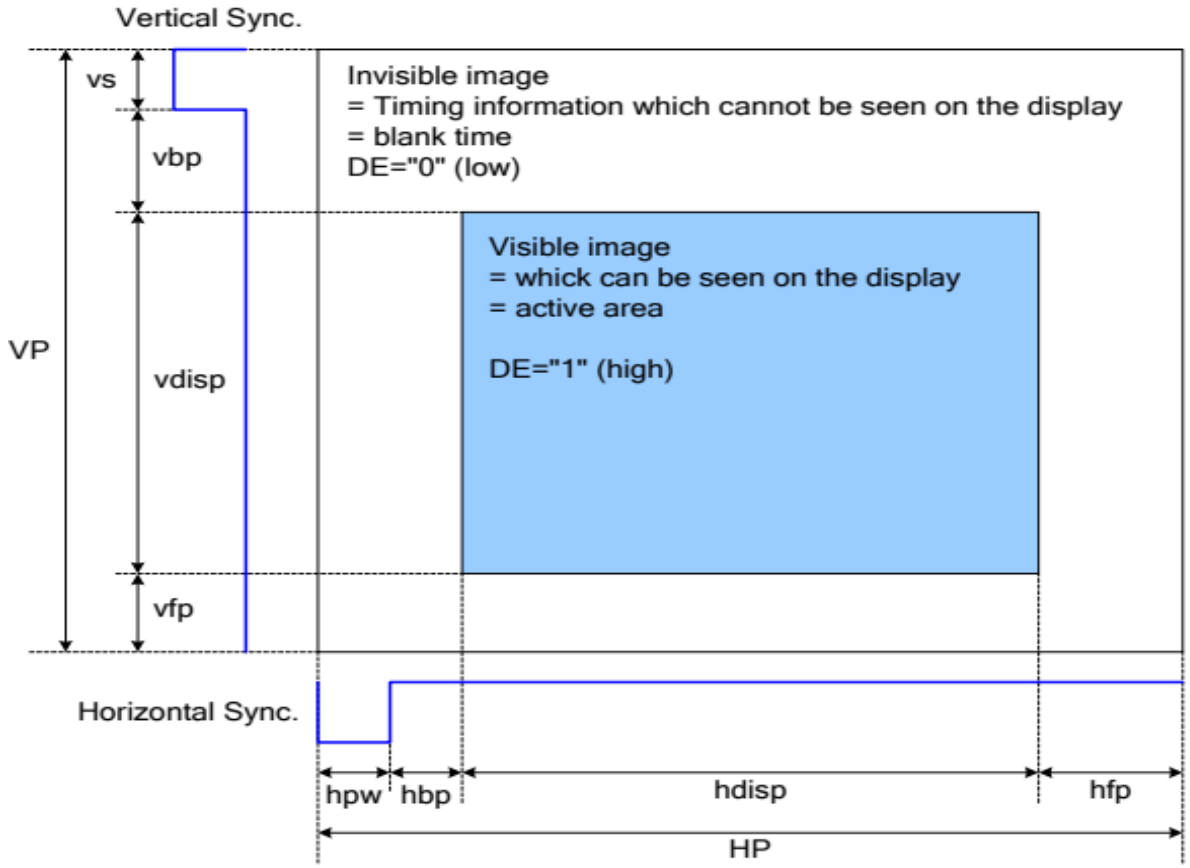


Figure 22 Access Area by RGB Interface

Please refer to the following table for the setting limitation of RGB interface signals.

Parameter	Symbol	Min.	Typ.	Max.	Unit
Horizontal Sync. Width	hpw	1	-	255	Clock
Horizontal Sync. Back Porch	hbp	1	--	255	Clock
Horizontal Sync. Front Porch	hfp	1	--	-	Clock
Vertical Sync. Width	vs	1	--	254	Line
Vertical Sync. Back Porch	vbp	1	--	254	Line
Vertical Sync. Front Porch	vfp	2	--	--	Line

Note:

1. *Typical value are related to the setting frame rate is 60Hz..*

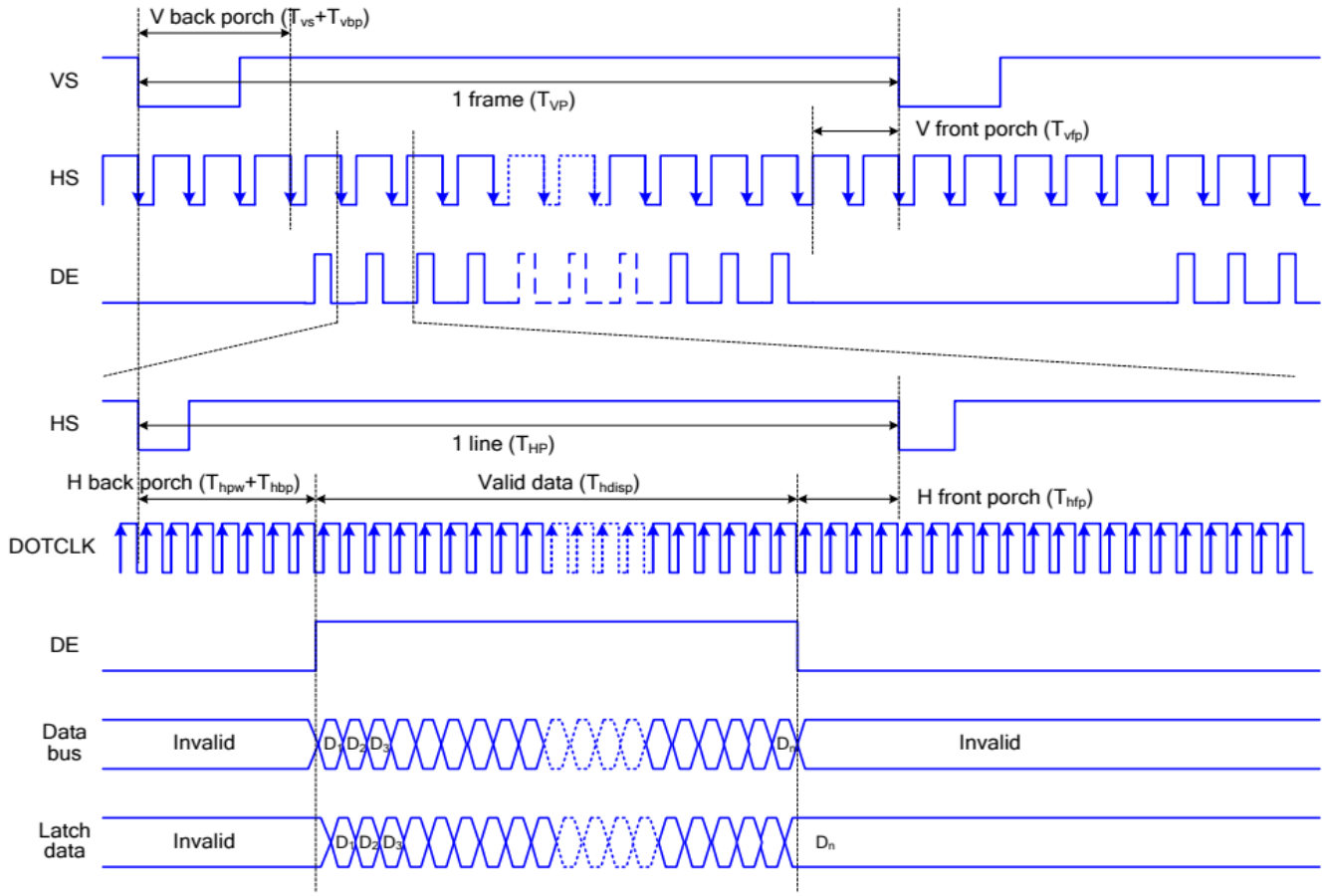
6.4 RGB Interface Mode Selection

Supports two kinds of RGB interface, DE mode and HV mode. The table shown below uses command C3h to select RGB interface mode.

DE/Sync	RGB Mode
0	DE mode
1	HV mode

RGB Interface Timing

The timing chart of RGB interface DE mode is shown as follows.



Note: The setting of front porch and back porch in host must match that in IC as this mode.

Figure 23 Timing Chart of Signals in RGB Interface DE Mode

The timing chart of RGB interface HV mode is shown as follows.

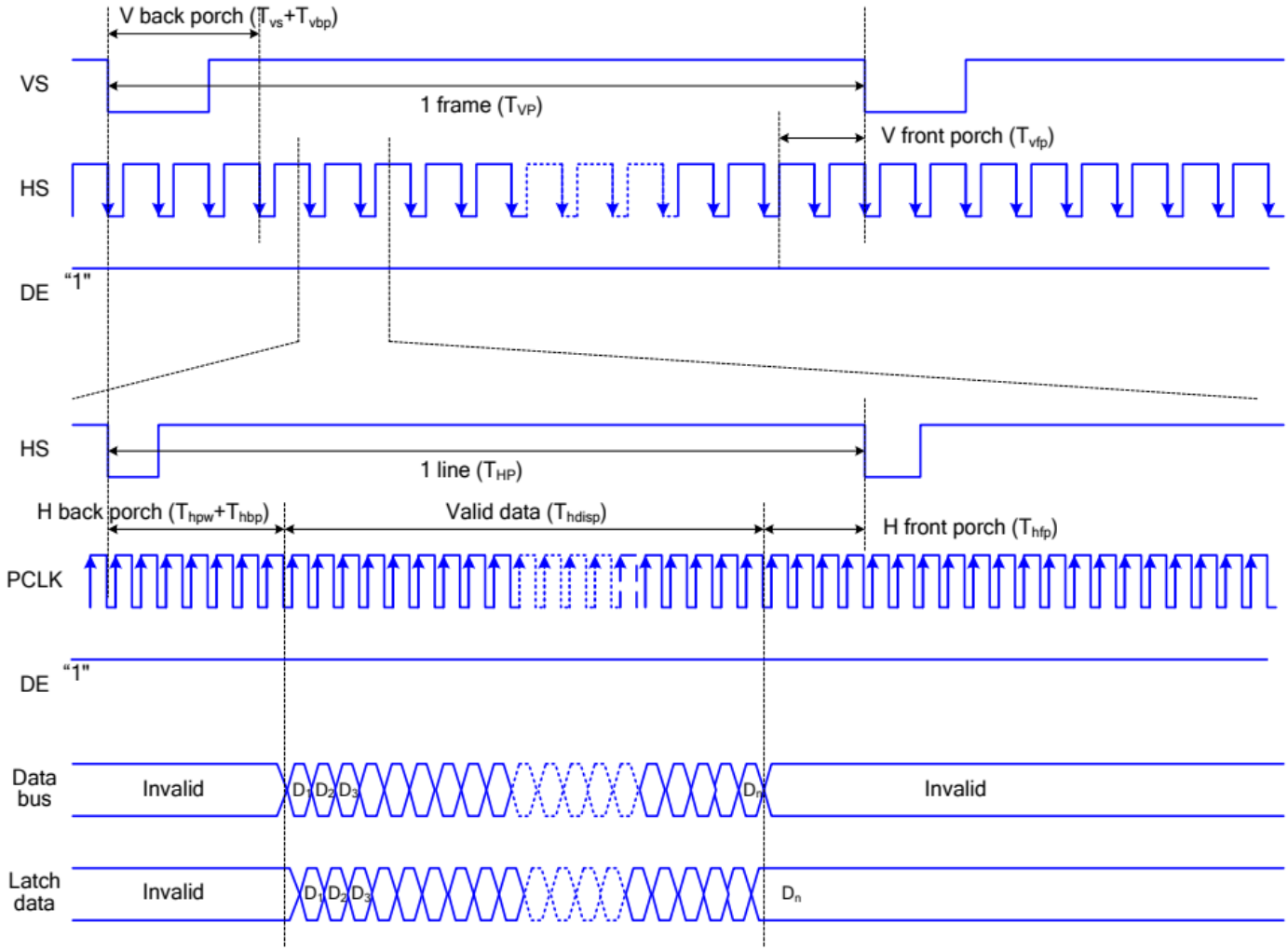


Figure 24 Timing chart of RGB interface HV mod

6.5 Reset Operation of IC

7.5.5 Reset Timing:

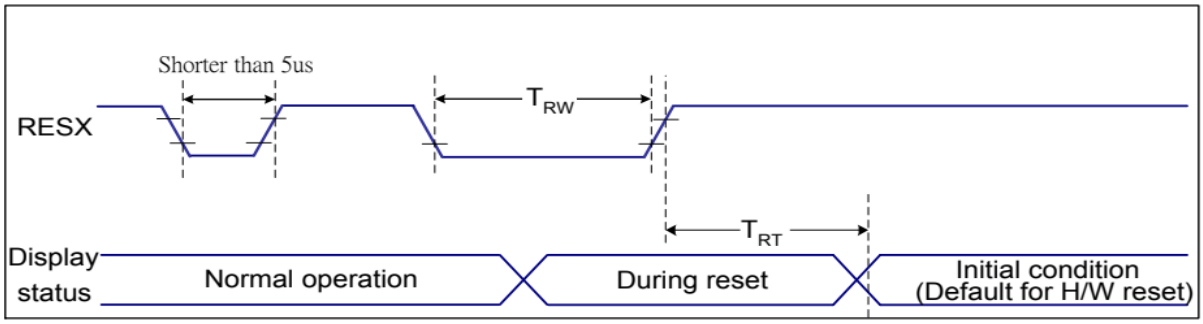


Figure 9 Reset Timing

VDDI=1.8,VDD=2.8, AGND=DGND=0V, Ta=25 ℃

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
				120(Note 1, 6, 7)	ms

Table 9 Reset Timing

Notes:

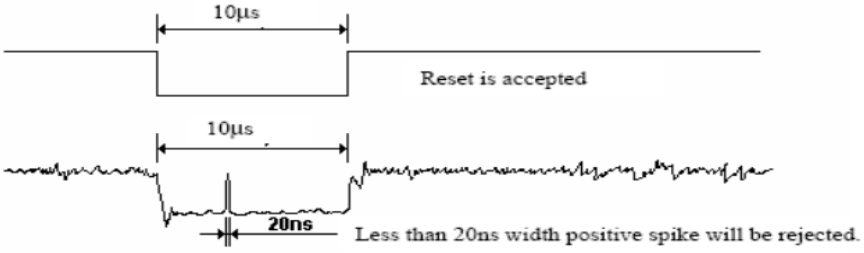
1. The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.

2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode.) and then return to Default condition for Hardware Reset.

4. Spike Rejection also applies during a valid reset pulse as shown below:



5. When Reset applied during Sleep In Mode.

6. When Reset applied during Sleep Out Mode.

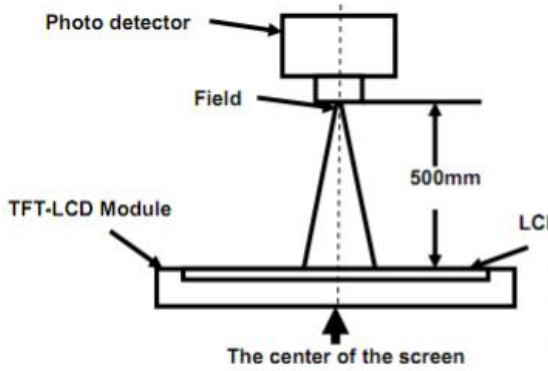
7. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

7 Optical Characteristics

Items		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing angles		θ_T	Center CR≥10	70	80	-	Degree.	Note2
		θ_B		70	80	-		
		θ_L		70	80	-		
		θ_R		70	80	-		
Contrast Ratio		CR	Θ =0	700	1000		-	Note1, Note3
Response Time		$T_{ON}+T_{OFF}$	25° C	-	25	35	ms	Note1, Note4
Chromaticity	White	X_W	Backlight is on	0.283	0.298	0.313	-	Note1, Note5
		Y_W		0.322	0.337	0.352	-	
	Red	X_R		0.644	0.659	0.674	-	
		Y_R		0.308	0.323	0.338	-	
	Green	X_G		0.260	0.275	0.80	-	
		Y_G		0.565	0.580	0.595	-	
	Blue	X_B		0.145	0.160	0.175	-	
		Y_B		0.140	0.155	0.170	-	
Uniformity		U		75	80	-	%	Note1, Note6
NTSC				55	60		%	Note5
Luminance		L		250	300	-	CD/M^2	Note1, Note7

Test Conditions:
1. IF= 40mA(one channel),the ambient temperature is 25°C.
2. The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.
The optical characteristics should be measured in dark room. After 5 minutes operation, the optical Properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio	BM-7A	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	SR-3A	2°

Note 2: Definition of viewing angle range and measurement system.
Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80).

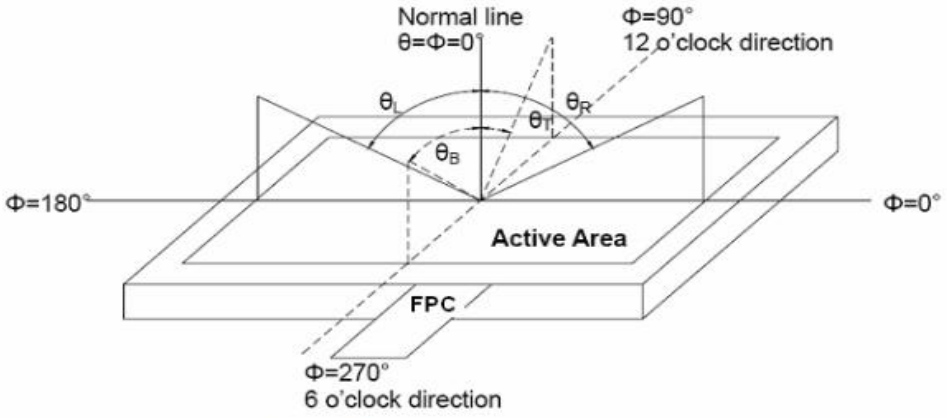


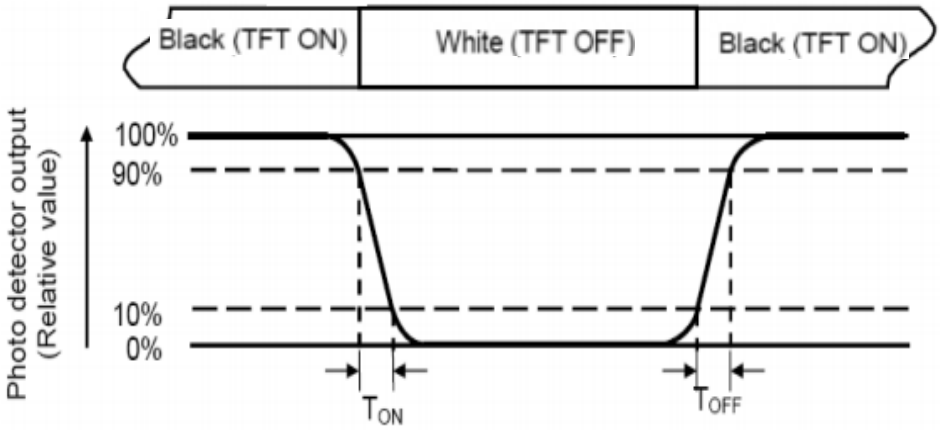
Fig. 1 Definition of viewing angle

Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval Between “White” state and “Black” state. Rise time (TON) is the time between Photo detector output intensity changed from 10% to 90%. And fall time (TOFF) is The time between photo detector output intensity changed from 90% to 10%



Note 5: Definition of color chromaticity (CIE1931)
Color coordinates measured at center point of LCD.
Note 6: Definition of Luminance Uniformity
Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the Center of each measuring area
Luminance Uniformity (U) = Lmin/ Lmax X100%
L-------Active area length W----- Active area width

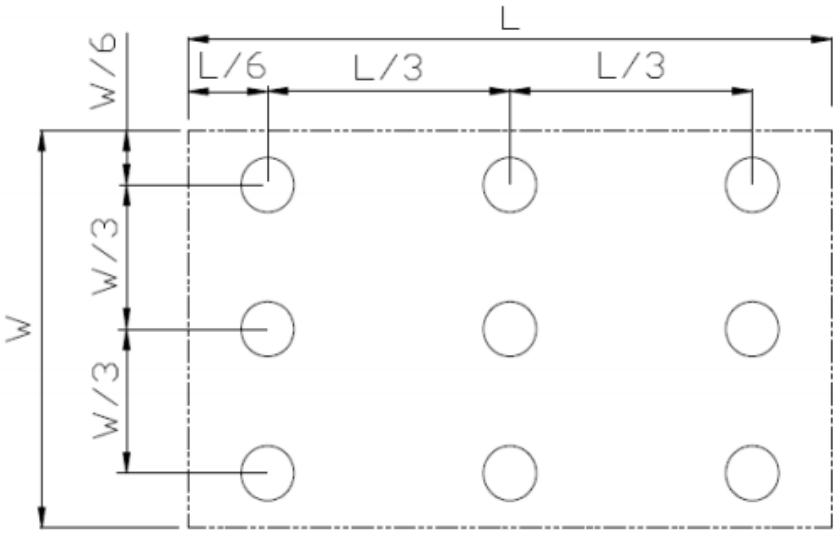


Fig. 2 Definition of uniformity
Lmax: The measured maximum luminance of all measurement position.
Lmin: The measured minimum luminance of all measurement position.
Note 7: Definition of Luminance:
Measure the luminance of white state at center point.

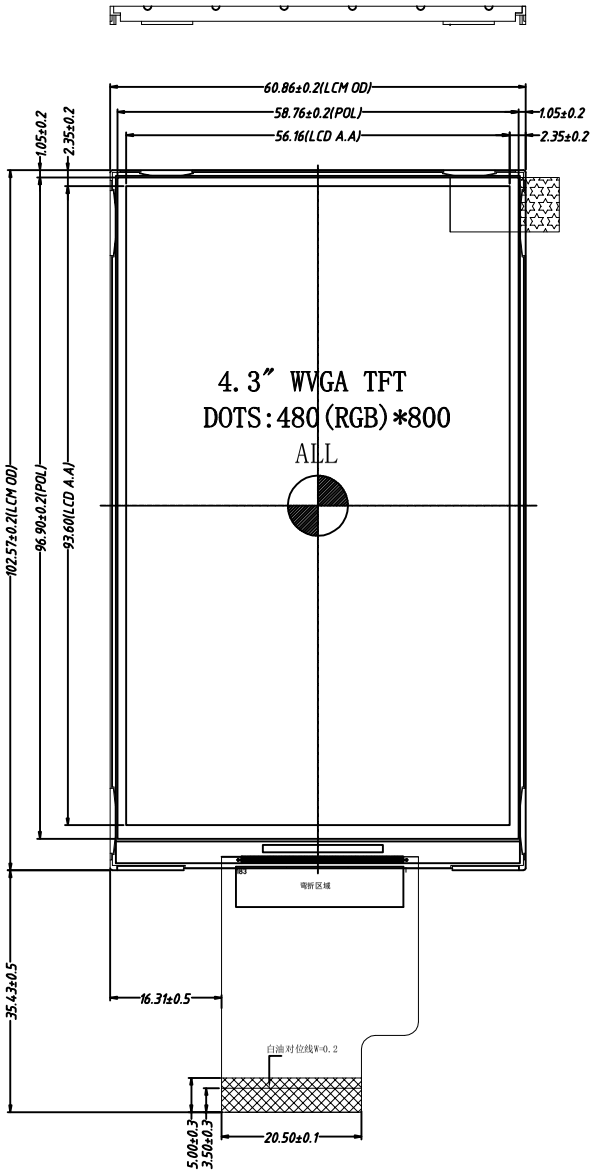
8 Environmental / Reliability Tests

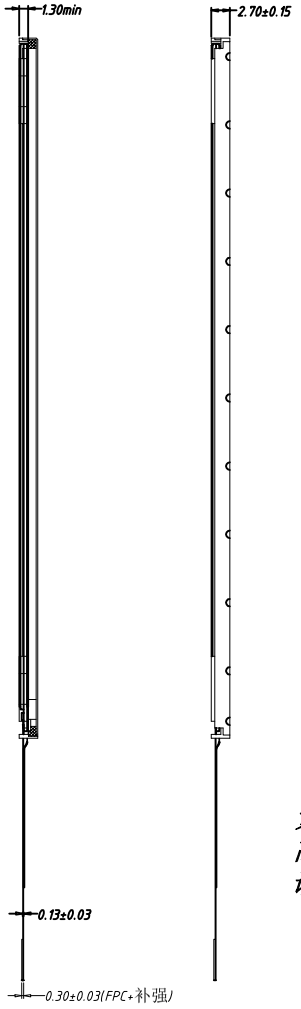
No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts= +70℃, 240hrs	Note 1 IEC60068-2-2, GB2423. 2-89
2	Low Temperature Operation	Ta= -20℃, 240hrs	Note 2 IEC60068-2-1 GB2423.1-89
3	High Temperature Storage	Ta= +80℃, 240hrs	IEC60068-2-2 GB2423. 2-89
4	Low Temperature Storage	Ta= -30℃, 240hrs	IEC60068-2-1 GB/T2423.1-89
5	High Temperature & Humidity Storage	Ta= +60℃, 90% RH max, 160 hours	IEC60068-2-3 GB/T2423.3-2006
6	Thermal Shock (Non-operation)	-30℃ 30 min ~ +80℃ 30 min Change time: 5min, 30 Cycle	Start with cold temperature, end with high temperature IEC60068-2-14, GB2423.22-87
7	Electro Static Discharge (Operation)	C=150pF, R=330 Ω, 5 points/panel Air:±8KV, 5 times; Contact: ±4KV, 5 times; (Environment: 15℃ ~ 35℃, 30% ~ 60%, 86Kpa ~ 106Kpa)	IEC61000-4-2 GB/T17626.2-1998
8	Vibration (Non-operation)	Frequency range: 10~55Hz, Stroke: 1.mm Sweep: 10Hz~55Hz~10Hz 2 hours for each direction of X .Y. Z. (package condition)	IEC60068-2-6 GB/T2423.5-1995
9	Shock (Non-operation)	60G 6ms, ± X, ±Y , ± Z 3 times for each direction	IEC60068-2-27 GB/T2423.5-1995
10	Package Drop Test	Height: 60 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8-1995

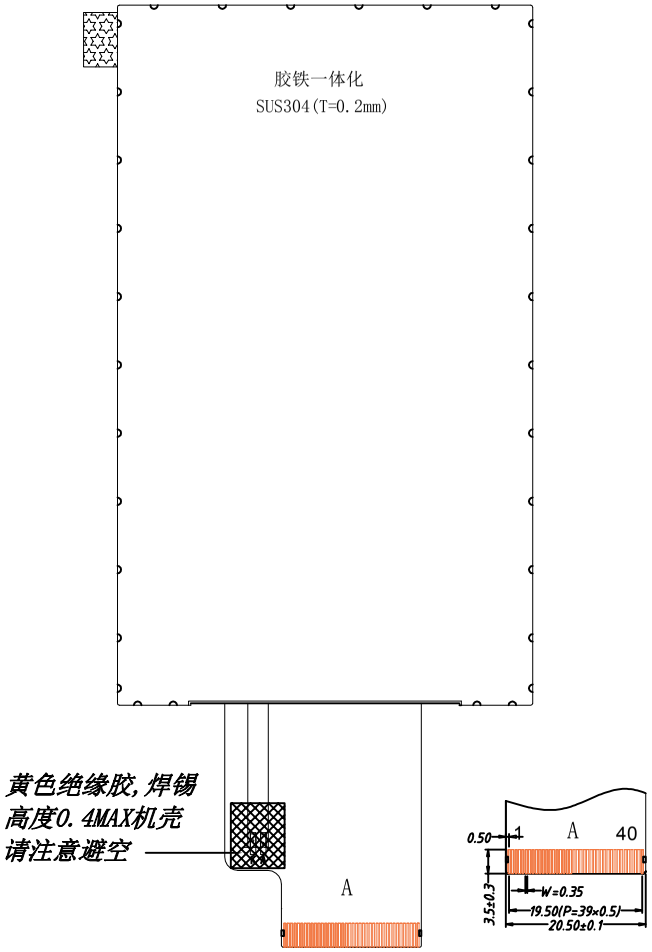
Note: 1. T_S is the temperature of panel's surface.
2. Ta is the ambient temperature of sample.

9 Mechanical Drawing

A0	2023/12/06	First issue





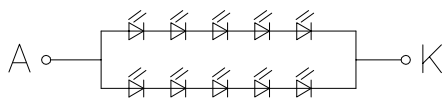


FPC展开出货

PIN	SYMBLE
1	LEDK
2	LEDA
3	GND
4	VCC
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	CLK
31	DISP
32	HSYNC
33	VSYNC
34	DE
35	NC
36	GND
37	RESET
38	SDA
39	SCL
40	CS

NOTES:
1. DISPLAY TYPE: 3.97'' INCH TFT
2. BACKLIGHT: 8 CHIP WHITE LED, IN PARALLEL
3. OPERATING TEMP: -20° C~+70° C
4. STORAGE TEMP: -30° c~+80° c
5. RESOLUTION: 480xRGBx800
6. Luminous instensity(9 AVG):Module:250 cd/m2(MIN) 300cd/m2(TYP.)
7. LCD IC:ST7701S
8. "( )"reference dimension. "*"critical dimension
9. RoHs Compliant

LED CIRCUIT DIAGRAM



IF=40MA, VF=14.5~16.5V

INTERFACE	RGB Interface	深圳市勤瑞光电科技有限公司 Xinrui photoelectric technology (shenzhen) CO.,LTD.	
	FPC Connector	MODEL NAME TFT Display Module	PART NO. H043BWV40I3082
VIEWING DIRECTION	ALL	DWN	REV. 1.0 / SHEET OF 1/1
		CHKD	TOLERANCE UNLESS SPECIFIED ±0.3
Gray Scale DIRECTION	FREE	PROJECTION 3RD ANGLE	UNIT mm / SCALE 1:1

1 0.Packing

TBD

11. Precautions for Use of LCD modules

11.1 Handling Precautions

11.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

11.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

11.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

11.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

11.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:
Water ； Ketene ； Aromatic solvents

11.1.6. Do not attempt to disassemble the LCD Module.

11.1.7. If the logic circuit power is off, do not apply the input signals.

11.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

11.1.8.1. Be sure to ground the body when handling the LCD Modules.

11.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

11.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

11.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

11.2 Storage Precautions

11.2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

11.2.2. The LCD modules should be stored under the storage temperature range If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ～ 40℃ Relatively humidity: ≤80%

11.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

11.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.