



Product Specification

Customer: _____
Model Name: H013BQ16I2502-CT8
Date: 2022-11-19
Version: A1

Preliminary Specification

Final Specification

For Customer's Acceptance

| Approved by | Comment |
|-------------|---------|
| | |

| Approved by | Reviewed by | Prepared by |
|-------------|-------------|-------------|
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Xunrui Shenzhen Optoelectronics Technology Co., Ltd.

Contents

| | |
|---|----|
| 1.Record of Revision..... | 3 |
| 2 .General Specifications..... | 4 |
| 3 .Input/Output Terminals..... | 5 |
| 4. Absolute Maximum Ratings..... | 6 |
| 5 .Electrical Characteristics..... | 6 |
| 6 .Interface Timing..... | 7 |
| 7. Optical Characteristics..... | 9 |
| 8 . Environmental / Reliability Tests..... | 13 |
| 9. Mechanical Drawing..... | 14 |
| 10.Packing..... | 15 |
| 11. Precautions For Use of LCD modules..... | 16 |



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2 General Specifications

| | Feature | Spec |
|-----------------|--------------------------|-------------------------------|
| Characteristics | LCD Size | 1.3 inch |
| | Display Format | 240 (RGB) ×240 |
| | Interface | SPI |
| | Color Depth | 262 K |
| | Technology type | a-Si |
| | Display Spec. | / |
| | Display Mode | Normally Black |
| | Driver IC | ST7789V |
| | Surface Treatment | HC |
| | Viewing Direction | ALL |
| | Gray Viewing Direction | FREE |
| Mechanical | LCM+CTP (W x H x D) (mm) | 33.12 (W) ×37.08 (H)×3.48(T) |
| | Active Area(mm) | 23.4(W) ×23.4(H) mm |
| | With /Without TSP | With TSP |
| | Weight (g) | TBD |
| | LED Numbers | 2 LEDs |

Note 1: Viewing direction is following the data which measured by optics equipment.

Note 2: Requirements on Environmental Protection: RoHS

Note 3: LCM+CTP weight tolerance: +/- 5%



3 Input/Output Terminals

| NO. | SYMBOL | Description | I/O |
|-----|----------|-------------------------------|--------------|
| 1 | LEDK | LED backlight input (Cathode) | Power supply |
| 2 | LEDA | LED anode | Power supply |
| 3 | VDD | Power supply | Power supply |
| 4 | GND | System Ground | Power supply |
| 5 | D/C | Data/Commander selection | I/O |
| 6 | CS | Chip select | I/O |
| 7 | SCL | Serial Data | I/O |
| 8 | SDA | Serial Clock. | I/O |
| 9 | RST | Reset PIN | I/O |
| 10 | CTP_3.3V | TP Power Supply | Power supply |
| 11 | CTP_SCL | Touch I2C clock | I/O |
| 12 | CTP_SDA | Touch I2C data | I/O |
| 13 | CTP_RST | TSP Reset signal. Active low. | I/O |
| 14 | CTP_INT | Touch State change interrupt | I/O |
| 15 | CTP_GND | System Ground | Power supply |
| 16 | GND | System Ground | Power supply |



4 Absolute Maximum Ratings

| Item | Symbol | MIN | MAX | Unit | Remark |
|-----------------------|------------------|------|----------------------|------|--------|
| Supply Voltage | V _{DD} | -0.3 | 4.6 | V | |
| Input Signal Voltage | V _{in} | -0.3 | V _{DD} +0.3 | V | |
| Logic Output Voltage | V _{OUT} | -0.3 | V _{DD} +0.3 | V | |
| Operating Temperature | T _{OPR} | -10 | 60 | °C | |
| Storage Temperature | T _{STG} | -20 | 70 | °C | |

5 Electrical Characteristics

5.1 Operating conditions:

| Parameter | Symbol | MIN | TYP | MAX | Unit | Remark |
|---------------------------|-----------------|-----|-----|-----|------|--------|
| Power Voltage | V _{DD} | 2.4 | 2.8 | 3.3 | V | |
| Digital Operation Current | I _{CC} | - | 8 | - | mA | |

5.2 Driving Backlight

| Item | Symbol | MIN | TYP | MAX | Unit | Remark |
|-------------------|-----------------|-----|-------|-----|------|--------------------|
| LED current | I _F | - | 40 | 50 | mA | Note 1 Note 2,3 |
| Power Consumption | | - | 145.9 | 165 | mW | |
| LED Voltage | V _F | 2.9 | 3.1 | 3.3 | V | |
| LED Life Time | W _{BL} | - | TBD | - | Hr | |

Note 1 Calculator Value for reference $I_{LED} \times V_{LED}/0.85 = P_{LED}$

Note 2 : There are 2 Groups LED

Note 3 : T_a = 25°C

Note 4 : Brightness to be decreased to 50% of the initial value



6 Interface Timing

6.1 AC Electrical Characteristics

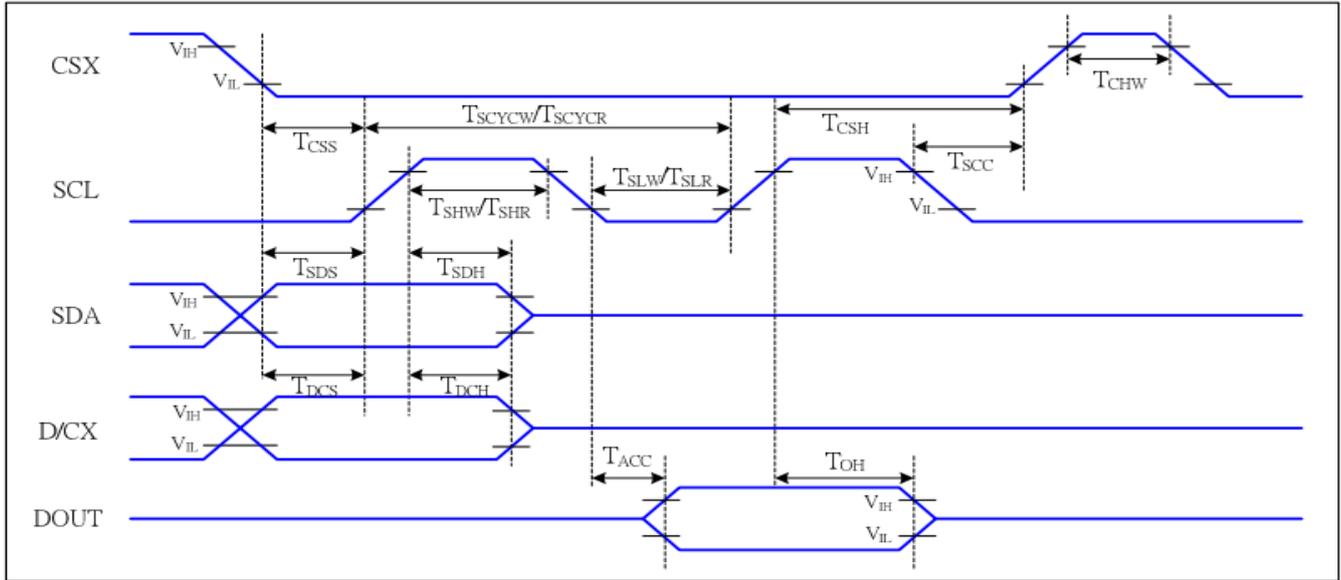
| Parameter | Symbol | Condition | Specification | | | Unit | Related Pins |
|-----------------------------------|---------|--------------------------|---------------|------|---------|------|--------------|
| | | | MIN. | TYP. | MAX. | | |
| Power & Operation Voltage | | | | | | | |
| System Voltage | VDD | Operating voltage | 2.4 | 2.75 | 3.3 | V | |
| Interface Operation Voltage | VDDI | I/O Supply Voltage | 1.65 | 1.8 | 3.3 | V | |
| Gate Driver High Voltage | VGH | | 12.2 | | 14.97 | V | Note 4 |
| Gate Driver Low Voltage | VGL | | -12.5 | | -7.16 | V | |
| Gate Driver Supply Voltage | | VGH-VGL | 19.36 | | 27.47 | V | Note 5 |
| Input / Output | | | | | | | |
| Logic-High Input Voltage | VIH | | 0.7VDDI | | VDDI | V | Note 1 |
| Logic-Low Input Voltage | VIL | | VSS | | 0.3VDDI | V | Note 1 |
| Logic-High Output Voltage | VOH | IOH = -1.0mA | 0.8VDDI | | VDDI | V | Note 1 |
| Logic-Low Output Voltage | VOL | IOL = +1.0mA | VSS | | 0.2VDDI | V | Note 1 |
| Logic-High Input Current | IIH | VIN = VDDI | | | 1 | uA | Note 1 |
| Logic-Low Input Current | IIL | VIN = VSS | -1 | | | uA | Note 1 |
| Input Leakage Current | IIL | IOH = -1.0mA | -0.1 | | +0.1 | uA | Note 1 |
| VCOM Voltage | | | | | | | |
| VCOM amplitude | VCOM | | | VSS | | V | |
| Source Driver | | | | | | | |
| Source Output Range | Vsout | | VAN | | VAP | V | |
| Gamma Reference Voltage(Positive) | VAP | | 4.45 | | 6.4 | V | Note 6 |
| Gamma Reference Voltage(Negative) | VAN | | -4.6 | | -2.65 | V | |
| Source Output Settling Time | Tr | Below with 99% precision | | | 20 | us | Note 2 |
| Output Offset Voltage | VOFFSET | | | | 35 | mV | Note 3 |

Notes:

1. TA= -30 to 70°C (to +85°C no damage).
2. Source channel loading= 2KΩ+12pF/channel, Gate channel loading=5KΩ+40pF/channel.
3. The Max. value is between measured point of source output and gamma setting value.
4. When evaluating the maximum and minimum of VGH, VDD=2.8V.
5. The maximum value of |VGH-VGL| can no over 30V.
6. Default register setting of Vcom and Vcomoffset is 20h



6.2 Timing chart



$V_{DDI}=1.65$ to $3.3V$, $V_{DD}=2.4$ to $3.3V$, $AGND=DGND=0V$, $T_a=-30$ to 70 °C

| Signal | Symbol | Parameter | MIN | MAX | Unit | Description |
|-----------|-------------|--------------------------------|-----|-----|------|---------------------------|
| CSX | T_{CSS} | Chip select setup time (write) | 15 | | ns | |
| | T_{CSH} | Chip select hold time (write) | 15 | | ns | |
| | T_{CSS} | Chip select setup time (read) | 60 | | ns | |
| | T_{SCC} | Chip select hold time (read) | 65 | | ns | |
| | T_{CHW} | Chip select "H" pulse width | 40 | | ns | |
| SCL | T_{SCYCW} | Serial clock cycle (Write) | 66 | | ns | -write command & data ram |
| | T_{SHW} | SCL "H" pulse width (Write) | 15 | | ns | |
| | T_{SLW} | SCL "L" pulse width (Write) | 15 | | ns | |
| | T_{SCYCR} | Serial clock cycle (Read) | 150 | | ns | -read command & data ram |
| | T_{SHR} | SCL "H" pulse width (Read) | 60 | | ns | |
| | T_{SLR} | SCL "L" pulse width (Read) | 60 | | ns | |
| D/CX | T_{DCS} | D/CX setup time | 10 | | ns | |
| | T_{DCH} | D/CX hold time | 10 | | ns | |
| SDA (DIN) | T_{SDS} | Data setup time | 10 | | ns | |
| | T_{SDH} | Data hold time | 10 | | ns | |
| DOUT | T_{ACC} | Access time | 10 | 50 | ns | For maximum $CL=30pF$ |
| | T_{OH} | Output disable time | 15 | 50 | ns | For minimum $CL=8pF$ |

Note : The rising time and falling time (T_r , T_f) of input signal are specified at 15 ns or less.
Logic high and low levels are specified as 30% and 70% of V_{DDI} for Input signals.



7 Optical Characteristics

| Items | Symbol | Condition | Min. | Typ. | Max. | Unit | Remark | |
|----------------------|------------|------------------------|-------|-------|-------|---------|-----------------|-----------------|
| Viewing angles | θ_T | Center CR \geq 10 | - | 80 | - | Degree. | Note2 | |
| | θ_B | | - | 80 | - | | | |
| | θ_L | | - | 80 | - | | | |
| | θ_R | | - | 80 | - | | | |
| Contrast Ratio | CR | $\Theta = 0$ | - | 800 | - | - | Note1, Note3 | |
| Response Time | T_{ON} | 25°C | - | 25 | 35 | ms | Note1, Note4 | |
| | T_{OFF} | | - | 25 | 35 | | | |
| Chromaticity | White | Backlight is on | X_W | 0.282 | 0.312 | 0.342 | - | Note1, Note5 |
| | | | Y_W | 0.339 | 0.369 | 0.399 | - | |
| | Red | | X_R | 0.560 | 0.639 | 0.669 | - | |
| | | | Y_R | 0.314 | 0.344 | 0.384 | - | |
| | Green | | X_G | 0.264 | 0.294 | 0.324 | - | |
| | | | Y_G | 0.507 | 0.587 | 0.617 | - | |
| | Blue | | X_B | 0.102 | 0.132 | 0.162 | - | |
| | | | Y_B | 0.106 | 0.136 | 0.166 | - | |
| Uniformity | U | | - | 80 | - | % | Note1, Note6 | |
| NTSC | | | 43 | 48 | - | % | Note5 | |
| LCM+CTP Luminance | L | | - | 150 | - | | Note1, Note7 | |

Test Conditions:

1. IF= 40mA(one channel),the ambient temperature is 25°C.
2. The test systems refer to Note 1 and Note 2.

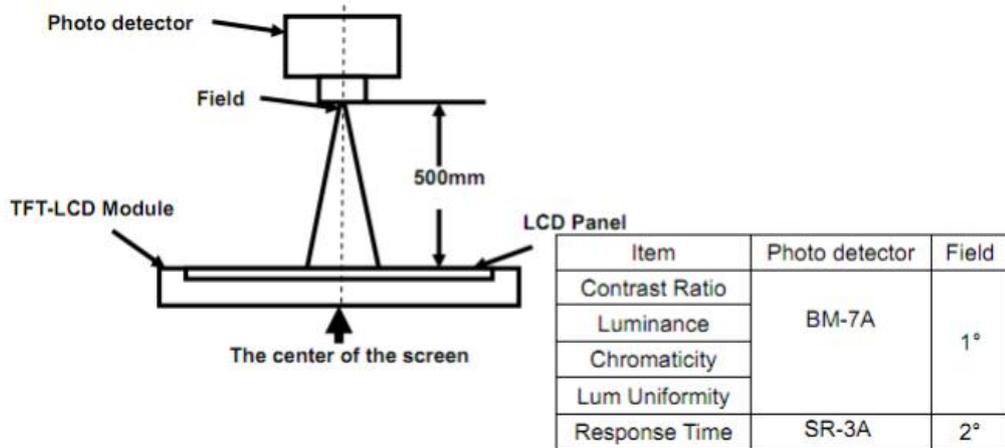
Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical Properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



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Note 2: Definition of viewing angle range and measurement system.
Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80).

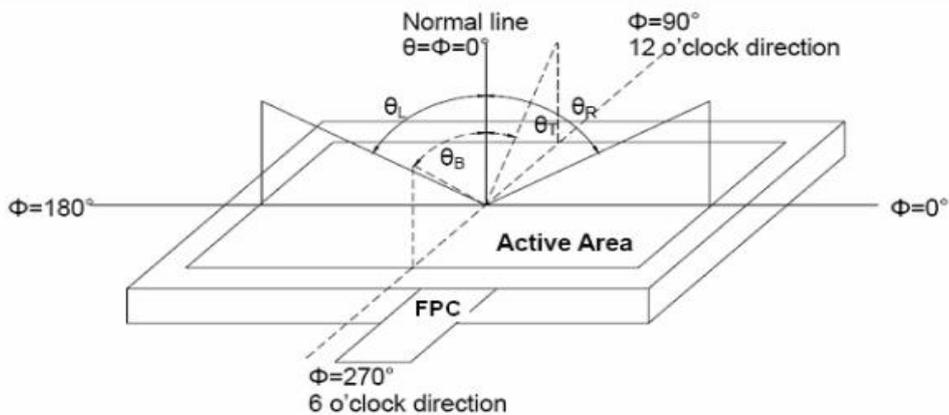


Fig. 1 Definition of viewing angle

Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

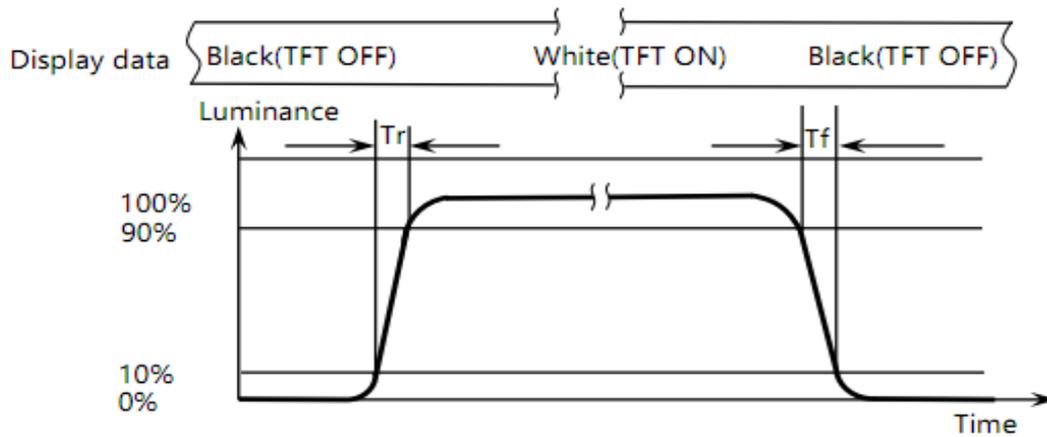
Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval Between "White" state and "Black" state. Rise time (TON) is the time between Photo detector output intensity changed from 10% to 90%. And fall time (TOFF) is The time between photo detector output intensity changed from 90% to 10%



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Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the Center of each measuring area

Luminance Uniformity (U) = $L_{min} / L_{max} \times 100\%$

L-----Active area length W----- Active area width

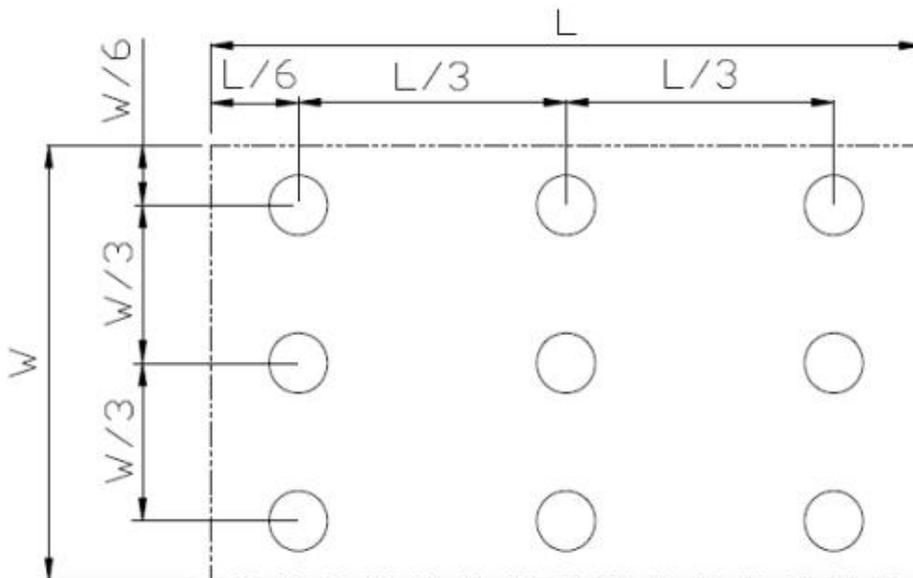


Fig. 2 Definition of uniformity

L_{max} : The measured maximum luminance of all measurement position.

L_{min} : The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.



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8 Environmental / Reliability Tests

| No | Test Item | Condition | Remarks |
|----|--------------------------------------|--|--|
| 1 | High Temperature Operation | T _s = +60°C, 240hrs | Note 1 IEC60068-2-2, GB2423. 2-89 |
| 2 | Low Temperature Operation | T _a = -10°C, 240hrs | Note 2 IEC60068-2-1 GB2423.1-89 |
| 3 | High Temperature Storage | T _a = +70°C, 240hrs | IEC60068-2-2 GB2423. 2-89 |
| 4 | Low Temperature Storage | T _a = -20°C, 240hrs | IEC60068-2-1 GB/T2423.1-89 |
| 5 | High Temperature & Humidity Storage | 60±5°C x 90%RH x 240Hours | IEC60068-2-3 GB/T2423.3-2006 |
| 6 | Thermal Shock (Non-operation) | -10°C 30 min ~ +60°C 30 min Change time: 5min, 30 Cycle | Start with cold temperature, end with high temperature IEC60068-2-14, GB2423.22-87 |
| 7 | Electro Discharge (Operation) Static | C=150pF, R=330 Ω, 5 points/panel Air:±8KV, 5 times; Contact: ±4KV, 5 times; (Environment: 15°C ~ 35°C, 30% ~ 60%, 86Kpa ~ 106Kpa) | IEC61000-4-2 GB/T17626.2-1998 |
| 8 | Vibration (Non-operation) | Frequency range: 10~55Hz, Stroke: 1.mm Sweep: 10Hz~55Hz~10Hz 2 hours for each direction of X .Y. Z. (package condition) | IEC60068-2-6 GB/T2423.5-1995 |
| 9 | Shock (Non-operation) | 60G 6ms, ± X, ±Y , ± Z 3 times for each direction | IEC60068-2-27 GB/T2423.5-1995 |
| 10 | Package Drop Test | Height: 60 cm, 1 corner, 3 edges, 6 surfaces | IEC60068-2-32 GB/T2423.8-1995 |

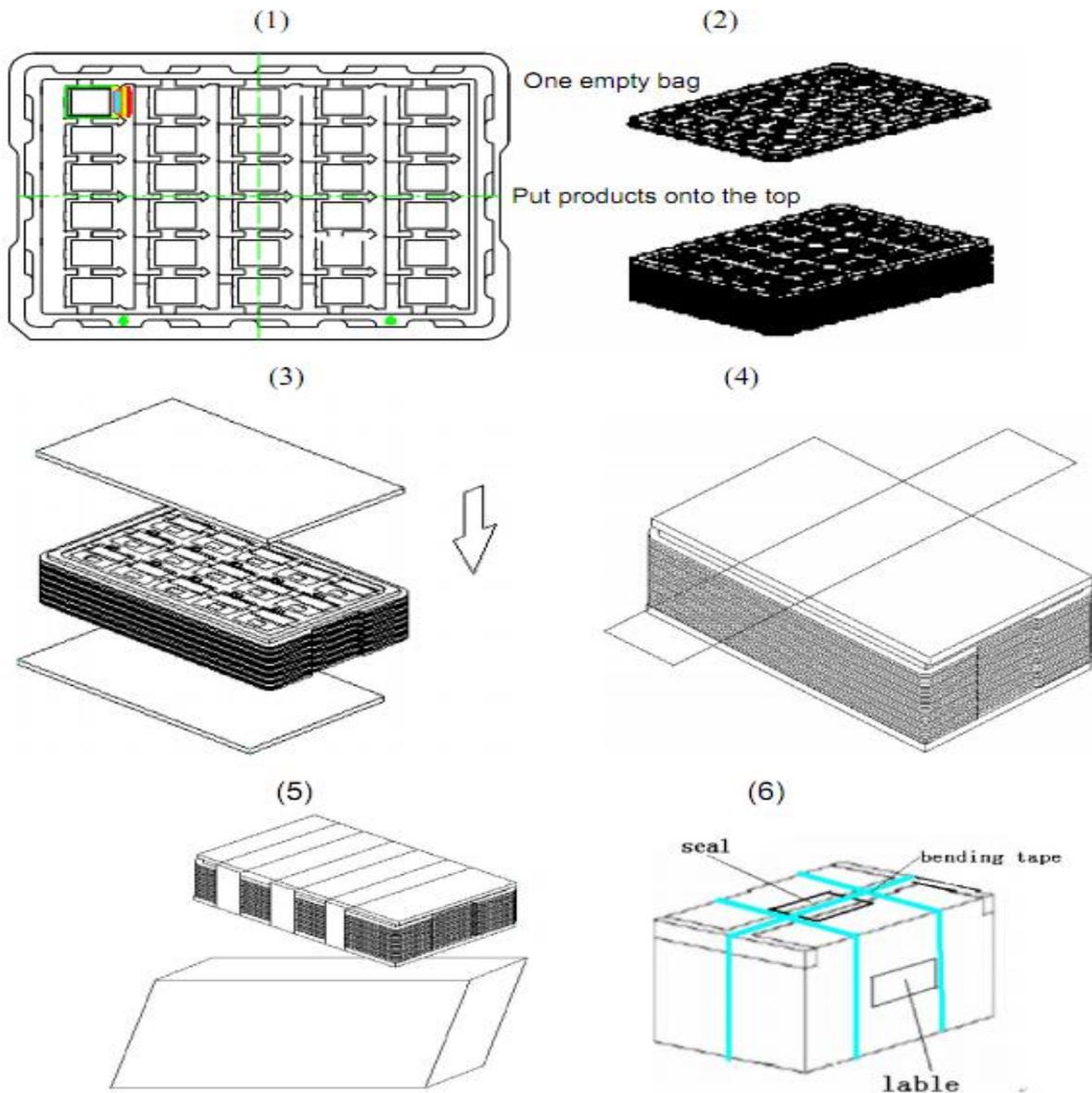
Note: 1. T_s is the temperature of panel's surface.

2. T_a is the ambient temperature of sample

9 Mechanical Drawing



1 0.Packing



1. Put module into tray cavity:
2. Tray stacking
3. Put 1 cardboard under the tray stack and 1 cardboard above:
4. Fix the cardboard to the tray stack with adhesive tape:
5. Put the tray stack into carton.
6. Carton sealing with adhesive tape.



11. Precautions for Use of LCD modules

11.1 Handling Precautions

11.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

11.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

11.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

11.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

11.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:
Water ; Ketene ; Aromatic solvents

11.1.6. Do not attempt to disassemble the LCD Module.

11.1.7. If the logic circuit power is off, do not apply the input signals.

11.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

11.1.8.1. Be sure to ground the body when handling the LCD Modules.

11.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

11.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

11.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

11.2 Storage Precautions

11.2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

11.2.2. The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ~ 40℃ Relatively humidity: ≤80%

11.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

11.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.